

10/516631

Attorney Docket No. P14162-US1

DT15 Rec'd PCT/PTO 01 DEC 2004
EUS/JP:04-2172

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Johannes Wilhelmus
Theodorus Eikenbroek

§ Group Art Unit: Unknown

Serial No: Unknown

§
§ Examiner: Unknown
§For: An Amplifier Circuit, Gyrator Circuit, Filter Device and Method for Amplifying a
SignalMail Stop PCT
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

"EXPRESS MAIL" Mailing Label No.: EV539111156US

Date of Deposit: December 1, 2004

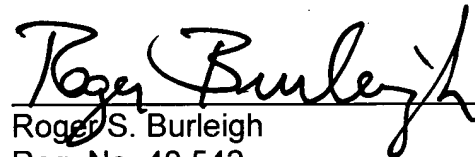
Dear Sir:

INFORMATION DISCLOSURE STATEMENT

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicant(s) hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Copies of the cited references were previously transmitted to the USPTO by the international searching authority.

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that the Examiner initial a copy of this form and return it to the undersigned.

Respectfully submitted,

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In place of PTO-1449 Form	U. S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	Complete if Known			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	Not Yet Known 10/516631		
		Filing Date	December 1, 2004		
		Applicant(s)	Theodorus Wilhelmus Johannes Eikenbroek		
		Art Unit	Not Yet Known		
		Examiner Name	Not Yet Known		
SHEET	1	OF	1	Attorney Docket Number	P14162-US1

U. S. PATENT DOCUMENTS				
Examiner's Initials	Cite No.	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
	AA	6 084 469 A	07/04/2000	Lorenz Perry
	AB	4 459 555 A	07/0/1984	Jett Jr. William B

FOREIGN PATENT DOCUMENTS					
Examiner's Initials	Cite No.	Foreign Patent Document <small>(Country Code - Number - Kind)</small>	Publication Date MM-DD-YYYY	Patentee or Applicant of Cited Document	Translation Y/N

OTHER PRIOR ART		
Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item, date, page(s), volume-issue number(s), publisher, city/country where published
	AC	DANYUK D L ET AL: "The Design of a Correction Circuit for Sectional Low-Noise Amplifiers", Telecommunications and Radio Engineering, Begell House, Inc., New York, NY, US, vol. 46, no. 12, December 1, 1991, pages 98-100, XP000311651, ISSN: 0040-2508, page 98-page 99
	AD	European Patent Office, International Search Report for PCT/NL02/00359, dated March 6, 2003

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include a copy of this form with next communication to applicant.